Patent claims

1.   A method for supplying power to a current sink (ED) via
two data line pairs of a local area network (LAN),
characterized
in that a feed voltage made available by a voltage source (SQ)
is injected between the center points of two voltage dividers
which are each arranged between the two data lines of a pair.

2.   The method as claimed in claim 1,
characterized
in that the feed voltage is output between the center points of
two voltage dividers which are each arranged between the two
data lines of a pair.

3.   The method as claimed in one of the preceding claims,
characterized
in that nonreactive resistors (R) in relation to the line
termination are used as the voltage dividers.

4.   The method as claimed in claim 3,
characterized
in that the resistors (R) used are equal in value.

5.   The method as claimed in one of the preceding claims,

characterized
in that the point between the two nonreactive resistors (R) is
used as the center point.

6.   The method as claimed in one of the preceding claims,
characterized
in that a circuit arrangement having electronic and/or passive
components is used as the voltage divider.

7.   An apparatus for supplying power to at least one current
sink (ED) via two data line pairs of a local area network
-      having two voltage dividers which are each arranged
       between the two data lines of a pair, and
-      having center points between the two voltage dividers for
       the purpose of injecting a feed voltage made available by
       a voltage source (SQ).

8.   The apparatus as claimed in claim 7,
characterized
in that the two voltage dividers, which are each arranged
between the two data lines of a pair, and the center points
between the two voltage dividers are provided for the purpose
of outputting a feed voltage made available by a voltage source
(SQ).

Patent claims

1.   A method for supplying power to a current sink (ED) via
two data line pairs of a local area network (LAN),
characterized
in that a feed voltage made available by a voltage source (SQ)
is injected between the center points of two voltage dividers
which are each arranged between the two data lines of a pair,
and
in that two nonreactive resistors (R), which are equal in
value, in relation to the line termination are used as the
voltage dividers.

2.   The method as claimed in claim 1,
characterized
in that the feed voltage is output between the center points of
two voltage dividers which are each arranged between the two
data lines of a pair.

3.   The method as claimed in one of the preceding claims,
characterized
in that the point between the two nonreactive resistors (R) is
used as the center point.

4.   The method as claimed in one of the preceding claims,
characterized
in that a circuit arrangement having electronic and/or passive
components is used as the voltage divider.

5.   An apparatus for supplying power to at least one current
sink (ED) via two data line pairs of a local area network
-      having two voltage dividers which are each arranged
       between the two data lines of a pair, and

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-     having center points between the two voltage dividers for
      the purpose of injecting a feed voltage made available by
      a voltage source (SQ),
-     having two nonreactive resistors (R), which are equal in
      value, in relation to the line termination as the voltage
      dividers.

6.   The apparatus as claimed in claim 5,
characterized
in that the two voltage dividers, which are each arranged
between the two data lines of a pair, and the center points
between the two voltage dividers are provided for the purpose
of outputting a feed voltage made available by a voltage source
(SQ).